



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jiang et al.

Serial No.: 10/706,576

Filed: November 12, 2003

For: METHODS FOR ASSEMBLING
MULTIPLE SEMICONDUCTOR DEVICES

Confirmation No.: 6662

Examiner: J. Dolan

Group Art Unit: 2813

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AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the specification begin on page 3 of this paper;

Amendments to the claims are set forth in the listing of the claims that begins on page 13 of this paper;

Corrections to the drawings are summarized on page 18 of this paper, with replacement sheets and annotated sheets showing the corrections enclosed herewith; and

Remarks start at page 19 of this paper.